fairlight ENGINEERING CHANGE NOTE: mfX3

ECN #:   385

ASSEMBLY:	BMW4061	**DESCRIPTION:**	MW4-AES1 Digital Input/Output
New Assembly:		**DATE:**	06/09/00
Current Assy Rev:	B	**Current Schematic Rev:**	1.1 2.0
New Assy Rev:	C	**New Schematic Rev:**	1.2 2.1

○ CMS BOMs Updated   ● **Excel BOMs Updated**   ● **Subcontractor Records Updated**   **Compatibility Maintained:** Yes

IMPLEMENTATION INFORMATION:

○ **Safety Issue**   ○ **UL Compliance Issue**   ○ **New Feature**   ○ **Cost Reduction**

● **Bug Fix**   ○ **EMC Compliance Issue**   ○ **Quality Issue**

○ **Reliability Reasons**   ○ **Software Change**   ○ **Cosmetic Change**

REASON FOR CHANGE:

To fix noise on AES inputs

(This change applies to both Rev A & Rev B PCBs)

DETAILS OF CHANGE:

Remove IC 74FCT244 at location U16.

Solder an IC 74LVC244 (P/N SSQ5981) at location U16.

Cut track from U16 pin 20 (between solder pad and via).

Connect a mod wire from U16/20 to U14/37.

Connect a 10nF SMD0805 cap (P/N CCE1338) between U16/1 and U16/20 with minimum length bare hookup wire.

For Rev A PCB: Put a solder blob across J3 and remove solder blob across J4 to raise the R&D Revision Code to 1.2.

For Rev B PCB: Put a solder blob across J4 to raise the R&D Revision Code to 2.1.

Place a assembly revision label on the card, marked "C", after the blank PCB P/N so it reads as " BMW4061A-C" (For Rev A PCB) and " BMW4061B-C" (For Rev B PCB).

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HW or Project Engineer……………………………………………………………………………

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R&D or Project Manager……………………………………………………………………………